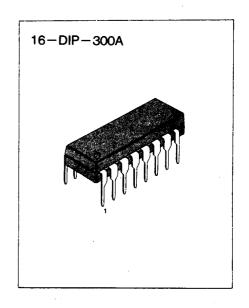
AM/FM TUNER

The KA2297 is a monolithic integrated circuit which consist of FM F/E + AM/FM IF and DET AMP.

The KA2297 is no adjustment AM/FM IF, DET coil

FEATURES

- Not need AM/FM IF, FM DET COIL
- Built-in FM Front End
- Minimum mumber of external parts required
- Operating voltage : V_{cc}=1.8V~7V



ORDERING INFORMATION

Device	Package	Operating Temperature		
KA2297	16-DIP-300A	−20~+70°C		

BLOCK DIAGRAM

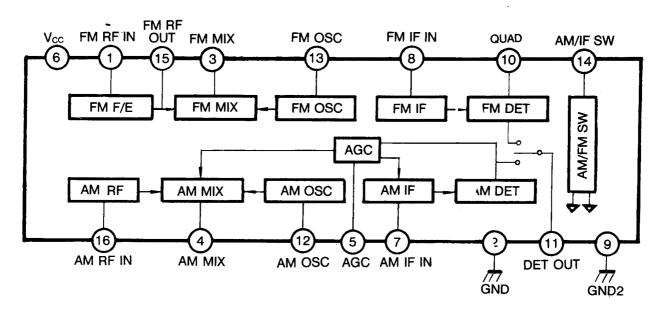


Fig.:1

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Characteristic	Symbol	Value	Unit
Maximum Supply Voltage	V _{cc}	8	V
Power Dissipation	P _d	250	mW
Operating Temperature	Topr	-20~+75	∘C
Storage Temperature	T _{stg}	-55~+125	°C

ELECTRICAL CHARACTERISTICS

(FM F/E: f=98MHz, fm=1KHz, FM IF: 10.7MHz, AM: f=1MHz, fm=1KHz, $\triangle f=30\%$, $V_{CC}=3V$

	Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Quiescent Circuit Current		Icca1	FM, V _i =0	6.0	10.0	14.0	mA
		lcca2	AM, V _i =0	3.0	5.0	8.0	mA
FM	-3dB Limiting	V _{(LIM)1}	V ₀ =-3dB Point		12	22	dΒμ
F/E							
FM	-3dB Limiting Sensitivity	V _{(LIM)2}	V ₀ =-3dB Point	42	47	52	dΒμ
IF	Detector Output Voltage	V _{O(DET) 1}	$V_i = 80dB\mu$	55	70	85	mV _{rms}
	Total Harmonic Distortion	THD ₁	V _i = 80dBμ		0.4	1	%
	Signal to Noise Ratio	S/N ₁	V _i = 80dBμ	56	62		dB
	AM Rejection Ratio	AMR	V _i = 80dBμ	32	38		dB
AM	Voltage Gain	G _{V1}	V _i = 30dBμ	28	50	72	mV _{rms}
	Detector Output Voltage	V _{O(DET) 2}	$V_i = 60dB\mu$	40	60	82	mV _{rms}
	Total Harmonic Distortion	THD₂	$V_i = 60 dB \mu$		1.0	2.0	%
	Signal to Noise Ratio	S/N ₂	$V_i = 60dB\mu$	37	43		dB

TEST CIRCUIT 1

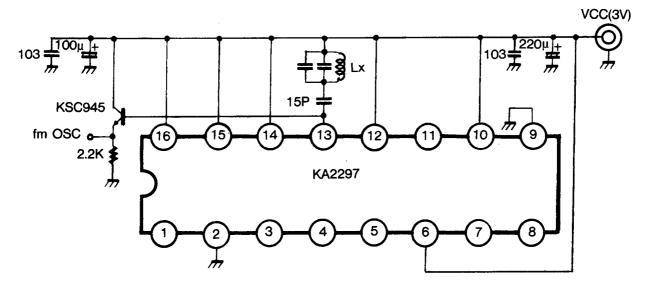


Fig. 2

TEST CIRCUIT 2

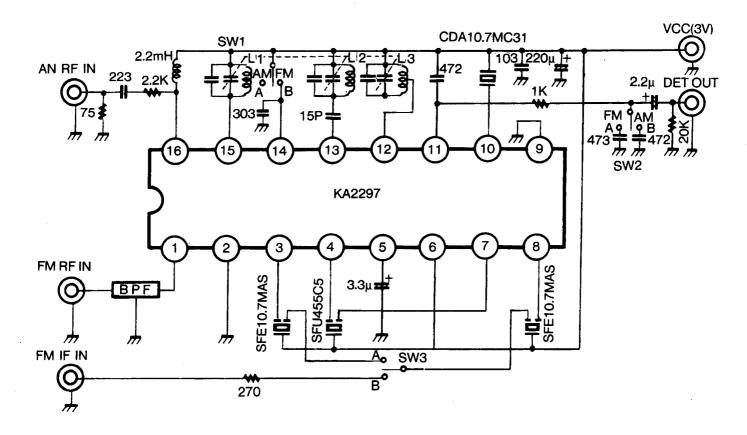


Fig. 3

COIL SPEC

SEAL NAME	L1		L2		L3		
TURNS	3-1	2 2/8	1-3	2 6/8	1-2	12T	
					2-3	73T	
WIRE(mmφ)	0.5	UEW	0.5	UEW	0.08	UEW	
CONNECTION (BOTTOM VIEV)	s	(4) (6) (6) (6) (6) (6) (6) (7) (7) (7) (7) (7) (7) (7) (7) (7) (7	(3) (4) (5) (5) (6) (7) (6) (7) (7) (7) (7) (7) (7) (7) (7) (7) (7		9 4 9 6 s 0 6		
FREQUENCY	100MHz		100	100MHz		797KHz	
TUNNG CAPACITY			-				
INDUCTANCE			2		26	268μH	
					± 8	%min	
UNLOADED Q					70)min	

APPLICATION CIRCUIT

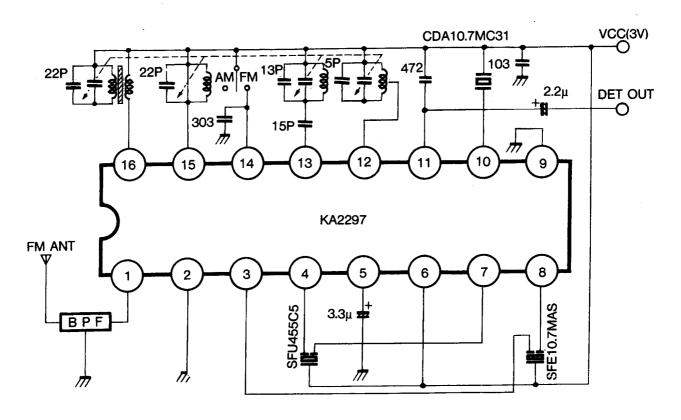


Fig. 4